

# Qpsk Modulator And Demodulator Using Fpga For Sdr

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### Qpsk Modulator And Demodulator Using

#### **QPSK Modulator and Demodulator Using FPGA for SDR**

Keywords-Demodulator, FPGA, Modulator, QPSK, SDR VHDL I INTRODUCTION The objective of this paper is to design a QPSK modem using FPGA for SDR (Software Defined Radio) In this paper the modulator and demodulator is implemented on single FPGA kit In which mainly concentrates on QPSK modulation techniques

#### **QPSK MODULATION AND DEMODULATION**

QPSK Modulator QPSK Demodulation: For QPSK demodulator , a coherent demodulator is taken as an example In coherent detection technique the knowledge of the carrier frequency and phase must be known to the receiver This can be achieved by using a PLL (phase lock loop) at the receiver A PLL essentially locks to the

#### **FPGA Implementation of /4 -QPSK Modulator and Demodulator**

approach to implement the QPSK modulator and demodulator [3] In this paper, the FPGA implementation of /4 QPSK modulator and demodulator is presented Complete modulator and demodulator units will be modeled using VHDL and functionality will ...

#### **Design of BPSK/QPSK Modulator using Verilog HDL and Matlab**

Quadrature Phase Shift Keying (QPSK) is a modulation After that the QPSK demodulator has been developed using MATLAB tool in order to verify the functionality of the modulator Keywords Binary Phase Shift Keying, Quadrature Phase Shift Keying, Design of BPSK/QPSK Modulator using Verilog HDL and Matlab

#### **QPSK Modulation and Demodulation - pudn.com**

The stated goal of this project was to design and implement QPSK Modulator and Demodulator The design consisted of two major sections: QPSK

Modulator and QPSK Demodulator This report outlines the design steps and outcome of the project, which include concept selection, functional specifications, and description of the final design 2

### **78-545617-01 Rev F DAVIC QPSK Demodulator Model D9492**

This chapter describes how the QPSK demodulator functions, and how the QPSK demodulator and the DAVIC QPSK modulator function together within the DBDS This chapter also includes illustrations and descriptions of the QPSK demodulator front and back panel components

### **DESIGN AND DEVELOPMENT OF A QPSK DEMODULATOR**

DESIGN AND DEVELOPMENT OF A QPSK DEMODULATOR 2 DECLARATION This is to certify that the thesis contains our original work towards the degree of

### **MODULATION AND DEMODULATION**

Therefore, this chapter begins with a discussion of quadrature phase shift keying (QPSK) and uses this discussion as a vehicle for development of generic models for quadrature modulation and demodulation The discussion then moves to binary phase shift keying ...

### **Design and Implementation of Bpsk Modulator and ...**

BPSK (Binary Phase Shift Keying) modulator and demodulator using Xilinx ISE 12.3 software BPSK consist of modulator a channel and demodulator The modulated signal was achieved in the first Spartan 3E board, passed through a channel and transmitted to the second board, which behaves as a

...

### **Implementation of FSK Modulation and Demodulation using ...**

Implementation of FSK Modulation and Demodulation using CD74HC4046A This application report discusses logic-level implementation of FSK modulator and demodulator using a PLL device HC/HCT4046A The HC/HCT4046A, PLL with VCO is a high-speed CMOS IC designed for 2

Implementation of FSK modulation and demodulation using SLAA618–November 2013

### **EMODULATOR IN WIRELESS COMMUNICATION SYSTEM ...**

phase component) So, the model of QPSK modulator and demodulator has two branches, the upper branch presents the In-phase component and the lower branch presents Quadrature-phase component A The QPSK Modulator Model and its Result The QPSK Modulator was simulated in Matlab using Simulink tool boxes The simulation model is given by Figure 1

### **Lecture 9 Modulation, Demodulation (Detection): Part 3**

Continuous-time QPSK Demodulator I & Q signals corrupted by noise Eye Diagram Eye Diagram Eye Diagram Eye Diagram 17 EE4900/EE6720

Digital Communications Suketu Naik Continuous-time QPSK: Eye Diagram QPSK 16-QAM QPSK 16-QAM  $\alpha=1$  Modulator (for testing demodulator)

### **VHDL IMPLEMENTATION OF QPSK MODULATOR & ...**

QPSK modulator and demodulator is attempted Complete modulator and demodulator units are modeled using VHDL, B Quadrature phase shift keying (QPSK) is a form of phase shift keying in which two bits are modulated at once, selecting one of four possible carrier phase shifts (0, 90, 180, or 270 degrees)

### **QPSK D9494 Demodulator Quick Reference Guide**

QPSK demodulator, power on the demodulator The QPSK modulator manages the QPSK demodulator, and provides the provisioning information from the DNCS to the demodulator after the demodulator is powered on),1\$/ Subject: Describes how to quickly install the QPSK D9494 demodulator into the ...

### **BPSK Demodulation for RF Applications**

BPSK Demodulation for RF Applications Yu-Ting Toh May 12, 2017 In this report, binary phase shift keying (BPSK) demodulation is discussed for an RF application Various methods are presented but the Costas loop is shown to be the most successful at obtaining

### **QPSK Modem Reference Design - Intel**

For digital modulator/demodulator applications, Altera provides a quadrature phase-shift keying (QPSK) modem reference design as an example design The QPSK modem reference design demo nstrates key features of the DSP Development Kit, Stratix Edition and DSP Development Kit, Stratix Professional Edition The QPSK modem reference design shows by

### **Binary Phase Shift Keying (BPSK) - Sonoma State University**

Quadrature Phase Shift Keying (QPSK) ! Using QPSK, we can double the data rate over the same The block diagram of a QPSK modulator is shown in Figure ! Binary data arriving at rate  $R_b$  The OQPSK demodulator is identical to that of QPSK demodulator except for a ...

### **Design of a low cost BPSK modulator/demodulator for a ...**

Design of a low cost BPSK modulator/demodulator for a practical teaching of digital modulation techniques Philippe DONDON- JM MICOULEAU- J LEGALL-PKADIONIK ENSEIRB, rue A Schweitzer- 33400 TALENCE PhilippeDondon@enseirbfr Abstract : Teaching the digital transmission techniques- like other fields of electronic- is not always easy : There is

### **PSK Demodulation (Part 1) - RF Cafe**

PSK Demodulation: Part 1 3 WJ Tech Notes 1984 Figure 1 BPSK and QPSK spectra PSK Modulation Techniques Although this article is concerned primarily with demodulation techniques involved in PSK systems, it will be helpful to also consider the encoding or modulation process A typical BPSK modulator is shown in Figure 2 Figure 2 BPSK modulator

### **A Binary Phase Shift Keying Demodulator using Pulse Detection**

A Binary Phase Shift Keying Demodulator using Pulse Detection Alexander Tan and Carlos E Saavedra Department of Electrical and Computer Engineering Queen 's Wniversiv Kingston, UN, Canada K7L 3N6 CarlosSaavedra@ecequeensuca Abstract - In this paper, a novel binary phase shift-keying demodulator is proposed and demonstrated